

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: **Derek Bernardon**

§ Group Art Unit:

Serial No.:

§ Examiner:

Filed: **November 21, 2003**

§ Attorney Docket No: **068758.0142**

Title: **Direct Current Voltage Converter
with Switching Regulator**

§ Client Ref.: **IO0229US/mgl/pp**

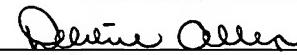
§

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING VIA EXPRESS MAIL

PURSUANT TO 37 C.F.R. § 1.10, I HEREBY CERTIFY THAT I HAVE INFORMATION AND A REASONABLE BASIS FOR BELIEF THAT THIS CORRESPONDENCE WILL BE DEPOSITED WITH THE U.S. POSTAL SERVICE AS EXPRESS MAIL POST OFFICE TO ADDRESSEE, ON THE DATE BELOW, AND IS ADDRESSED TO:

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INFORMATION DISCLOSURE STATEMENT

Sir:

Applicants respectfully request, pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, that the art listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified application. A copy of the cited art is enclosed for the convenience of the Examiner.

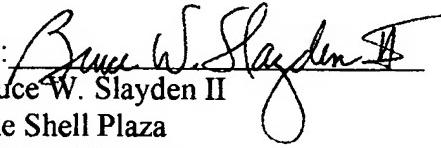
Furthermore, pursuant to 37 C.F.R. §§1.97(g) and (h), no representation is made that these references are material to the patentability of the present application.

As the Information Disclosure Statement is being submitted before the mailing of the first office action on the merits, Applicants believe that no fee is required. If a fee is

required, please accept this transmittal as a petition therefor and charge any fee to Baker Botts L.L.P. (*formerly, Baker & Botts, L.L.P.*) Deposit Account No. 02-0383, Order No. (068758.0142) for any other charges necessary for the filing of this Information Disclosure Statement.

BAKER BOTTS L.L.P. (023640)

Date: November 21, 2003

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PTO-1449 Information Disclosure Citation in an Application			Application No.	Applicant(s): Derek Bernardon			
			Docket Number 068758.0142	Group Art Unit	Filing Date November 21, 2003		
U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
1		3,781,642	12/25/73	Dutton	321	43	11/6/72
2		4,866,367	9/12/89	Ridley et al.	323	287	4/11/88
3		5,055,767	10/8/91	Nelson	323	285	6/29/90
4		5,514,947	5/7/96	Berg	323	282	1/31/95
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES
5		EP1 073 187 A2	7/26/00	EPO	H02M	3/156	x
6		JP11262248 A	9/24/99	JAPAN (ABSTRACT)	H02M	3/155	x
				JAPAN			
NON-PATENT DOCUMENTS							
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)				DATE	
7		"High Current Dual DC/DC Converter Operates from 3.3V Input"; A. Godil, Electronic Design, Penton Publishing, Cleveland, OH, Vol. 47, No. 25, pps. 176A-176B				12/6/99	
8		Linear Technology; "Dual 550Hz Synchronous 2-Phase Switching Regulator Controller", Linear Technology Corporation, Milpitas, CA				1999	
9		"Considerations in Designing the Printed Circuit boards of Embedded Switching Power Supplies"; M. Brown, Fairchild Semiconductor, pps. 1-12				4/30/00	
10		"Low Voltage, Precision Step-down Controller for Portable CPU Power", MAX 1636, pps. 1-22				2/98	
11		"12V or Adjustable, High-Efficiency, Low I _Q Step-Up DC-DC Controller, MAX 1771, pps. 1-12				7/95	
12		"Halbleiter-Schaltungstechnik", paragraph 16.6.2; "Erzeugung des Schaltsignals" by U. Tietze et al., Springer Verlag Berlin, 11th Edition				1999	
EXAMINER				DATE CONSIDERED			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.							